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(54) **Method of identifying several transponders**

(57) Transponders present in an interrogation zone can be identified by an interrogator by it sending an RF interrogation signal into the interrogation zone, the RF interrogation signal containing a code string prompting the transponders to generate partial addresses. As soon as one transponder "sees" that the generated partial address agrees with part of its own address, it responds by sending its full address which can then be read by the interrogator. Immediately after having received a full address the interrogator sends a code string characterizing the address of the transponder having responded before so that this transponder is thereby addressable. The signal sent by the interrogator to the transponder with this code string also contains an instruction which prompts the transponder to assume the condition in which it no longer responds to receiving its address or partial address.

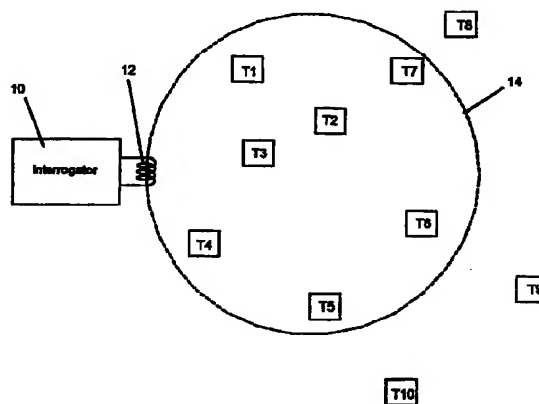


Fig.2

Description

[0001] The invention relates to a method of identifying several transponders located in the interrogation zone of a reader, as set forth in the preamble of claim 1.

[0002] More and more applications materialize in which transponders are used for identifying persons, animals or objects. Stored in these transponders is an identification code in the form of an address containing for example 32 or 64 bits which is the ID of the transponder carrier. In addition, data may be stored in the transponder containing specific information as to the carrier of the transponder. A transponder suitable for such purposes is described for example in EP-B-0 301 127, the specific feature of which is that it has no supply voltage source of its own, it instead generating its energy needed for sending the information and data contained therein from an RF interrogation pulse which it receives from an interrogator. The interrogator sends the RF interrogation pulse every time it wishes to retrieve information and data stored in a transponder existing in the interrogation zone. On receiving the RF interrogation pulse the transponder responds by sending the information and data stored therein.

[0003] In more sophisticated transponder systems the interrogator sends an RF interrogation pulse not only so that a transponder in the interrogation zone responds but it is desired to address the transponders present in the interrogation zone individually, i.e. to also send their address from the interrogator so that only the transponder responds whose address agrees with the address sent by the interrogator. This makes it necessary for the interrogator to know, however, which transponders exist in the interrogation zone. Since the range of possible addresses and thus the number of transponders belonging in all to the transponder system is very high, it possibly amounting to several millions, for example, it is of course totally out of the question that the interrogator sends all the addresses one after the other and then waits to see whether a transponder responds. The time needed for this would be prohibitively long, so that such a system would be totally useless for many applications. When it is assumed, for example, that the transponders are located on objects moving past a reader on a flow line conveyor, then the reader needs to "see" relatively quickly which transponder is in its interrogation zone at the time so that it can address these transponders explicitly and retrieve the information and data stored therein. It is thus very important to identify all transponders existing in the interrogation zone of the reader as quickly as possible.

[0004] One known method with the aid of which this identifying can be implemented quickly and reliably is described in EP-A2-0 831 618. Referring now to Fig. 1 the following is a brief discussion of how this known identification method works.

[0005] In every interrogation step the interrogator sends an RF interrogation signal which is made use of

by the transponders existing in the interrogation zone - among other things - to generate the supply voltage needed for their operation. The first RF interrogation signal sent also includes a partial address containing 4 bits in the example described, this partial address having the value zero. These 4 bits may be, for example, the four least-significant bits of the full transponder address totalling 32 or also 64 bits. The transponders are configured so that they send their response signal when the partial address received by them corresponds to the partial address in the full address stored therein. Thus, when, as in the assumed case, the partial address sent by the interrogator has the value zero, then all transponders in the interrogation zone whose least-significant 4 bits likewise have the value zero would respond. In the example as shown in Fig. 1 no such transponder exists in the interrogation zone, this resulting in the interrogator incrementing the partial address by one step and sending an RF interrogation signal accompanied by the partial address having the value 1. For this case too, it is assumed in the example that no transponder exists in the interrogation zone in which the last 4 bits of its address have the value 1.

[0006] In the next step in interrogation the partial address sent by the interrogator is incremented to the value 2. In the interrogation zone as shown in Fig. 1 as the example two transponders exist whose 4 least-significant bit have the value 2. This results in these two transponders sending back at the end of the interrogation signal their response signal to the interrogator, this response signal containing the full address of the two transponders involved. However, the interrogator is unable to distinguish these two addresses, it instead merely being able to "see" that more than one transponder was present with the corresponding address since it received an unreadable response signal hash. The interrogator memorizes the fact that the partial address 2 produced a hit, it then continuing to increment and send the partial address in its value consecutively. After sending the partial address with the value 7 a transponder responds, the reader in this case being able to read the complete transponder address so that in this sequence cycle of the first partial address the address of a transponder has been identified. This is evident from Fig. 1, whereby the value 1837 is cited, for example, as the full address, i.e. the last number 7 agreeing with that of the partial address sent by the interrogator, it being this partial address that prompted the return of the full transponder address.

[0007] As mentioned, the reader has memorized that several transponders responded to sending the partial address with the value 2. To identify the full address of these two transponders the value of the partial address having resulted in the hit is used as a mask to which a partial address of 4 further bits is added. The mask has the effect that the 4 bits of the transponder address previously compared to the partial address are "masked", i.e. concealed so that it is not these but the

next 4 bits of the transponder address that are compared to the new partial address. The identification procedure is then continued, the mask having the value 2 in each case, whilst the added partial address runs through the sequence of values 0 to 15. In the example as shown in Fig. 1 a new hit occurs with the partial address having the value 3, whereas at value 4 only one transponder responds so that its address can be identified by the reader. Since the mask has the value 2 and the partial address at which the transponder responded had the value 4 the last two values of the full transponder addresses are the values 2 and 4 as evident from Fig. 1. A hit likewise occurs at the partial address value 7, as evident from Fig. 1, so that the interrogator memorizes that hits having occurred at partial address value 3 and partial address value 7. These hits need to form the basis of the further interrogation procedures in identifying the transponders involved.

[0008] The interrogator now knows that at least two transponders exist in which the mask value 2 and the partial address value 3 are involved. At least two further transponders exist in the interrogation zone which have the mask value 2 and the partial address value 7. This means, in one case, that the transponders have the value 32 at the end of their full addresses and, in the other case, the value 72. The combination of the mask used before and the partial address causing the hit forms for the new interrogation cycle a new mask totalling eight bits, whereby the mask value, in one case, amounts to 32 and, in the other case, to 72. In the subsequent consecutive interrogation procedures the new partial address is incremented in its value from 0 to 15. After sending an RF interrogation signal having the corresponding partial address it is again detected whether one or more transponders respond in the interrogation zone. As evident from Fig. 1 a transponder responds at the partial address value 8 so that this transponder can be identified from the address it sends back. Partial address value 5 again produces a hit, meaning that in the interrogation zone several transponders exist whose address matches the mask value 32 and the partial address 5.

[0009] In further testing for hits for the partial address value 7 a further partial address is added to the mask value 72, this partial address being likewise incremented from 0 to 15. As evident from Fig. 1 only one transponder responds in each case for partial address 8 and partial address 9 so that their addresses can be identified from the signal sent back by them.

[0010] The last hit still to be resolved for the partial address 5 at mask value 32 makes use of this partial address together with the mask used before, as the new mask so that the mask value is 532 and a new partial address is added, the value of which is again incremented from 0 to 15. Only one transponder responds in each case for both the partial address 1 and the partial address 9 so that their addresses too, can be identified. No further transponders exist in the interrogation zone

since no further hits have occurred.

[0011] When making use of this known method it may happen that two or more transponders respond at the same time to an RF interrogation signal in the interrogation zone of the interrogator without this being read as a hit by the reader. The reason for this may be that the transponders are located differently remote from the reader so that, for example, the transponder located very near to the reader strongly predominates by its response so that the responses of the transponders located further away are received so weakly that they are unable to swamp the strongly received response. To solve this problem it is provided for in the known method to send a signal - after having identified all transponders existing in the interrogation zone - to all identified transponders in making use of their addresses which puts them in the condition in which they do not output their response signal even when having received an address directed thereto. Once the identified transponders have been practically muted, a complete identification procedure can then be recycled in which also the previously "overheard" transponders can be detected.

[0012] However, this procedure of muting the transponders necessitates sending the full transmission message to each and every transponder identified which, in addition to the 32 or 64 bit address, also needs to include a start bit string, label, control characters, parameters, a check bit string and an end bit string. This procedure takes up a lot of time, relatively, which makes it impossible to use in all cases in which a relatively high number of transponders are located only briefly in the interrogation zone of the reader.

[0013] To speed up the identification procedure a method may be put to use as known from EP-A1-0 919 943. In this method the interrogator does not send one partial address after another to the interrogation zone, instead special measures are taken to ensure that the partial addresses are generated in the transponders and compared to the addresses actually stored in the transponder. For generating the partial addresses each transponder contains a counter whose capacity corresponds to the number of possible partial addresses. This counter is incremented one step at a time when the RF interrogation signal sent by the reader, which is made use of to generate the supply voltage in the transponders, is briefly interrupted. After every interruption pulse of this signal the counter thus has a certain count representing a partial address. In a comparator this count is compared to the address stored in the transponders, the transponder then sending back its response signal to the reader only when agreement has been found. This method avoids the time otherwise needed when the interrogator needs to implement every time a full addressing procedure in sending the full data message as needed for sending a transponder partial address to the interrogation zone.

[0014] The invention is based on the object of defining a method of the aforementioned kind with the aid of

which the addresses of all transponders existing in the interrogation zone can now be speedily and reliably identified.

[0015] In accordance with the invention this object is achieved in that the interrogator sends the at least one pulse for incrementing to the next partial address in sequence in the transponder directly on timeout of a time period in which the interrogator is waiting for a transponder response when it has received no transponder response, whilst when having received a transponder response it sends an RF interrogation signal containing a signal section to be recognized by the responding transponder as a code string characterizing its address and a data section, and it not being until thereafter that it sends at least one pulse activating the incrementation to the next partial address in sequence, and that in the data section an instruction is sent to the responding transponder which places it in the condition in which on receiving an RF interrogation signal containing its address does not react by sending its response signal.

[0016] When in implementing an identification procedure a transponder existing in the interrogation zone of the interrogator is identified, meaning that the interrogator has read the full address of the responding transponder, it is now no longer necessary for the interrogator in muting the transponder to send a full RF interrogation signal containing the complete address and a muting instruction to the transponder, it instead being sufficient to send a code string identifying the transponder address and is thus also interpreted by the transponder. The transponder thus "sees" itself addressed on receiving this code string and reacts to the muting instruction by changing into a resting status. It is in this way that a lot of time can be saved, especially when many transponders need to be signalled into the resting status, since having to send the complete RF interrogation signal is now avoided.

[0017] Advantageous further embodiments of the invention are characterized in the sub-claims.

[0018] The invention will now be detailed by way of an example with reference to the drawing in which:

Fig. 1 is a schematic illustration of the sequence in an identification procedure in accordance with prior art,

Fig. 2 is a schematic illustration of a transponder system in which the identification method in accordance with the invention is put to use,

Fig. 3 is a schematic illustration of the contents of a full RF interrogation signal sent by the interrogation signal and

Fig. 4 is a time plot helping to explain the principle sequence of the identification method in accordance with the invention.

[0019] Referring now to Fig. 2 there is illustrated a transponder system comprising an interrogator 10 which sends by means of an antenna 12 RF interrogation signals into a spatially defined interrogation zone 14, indicated in Fig. 2 by a broken line circle. This interrogation zone materializes from the fact that the interrogator 10 sends only at a relatively low power so that accordingly only a defined zone also exists in which the RF interrogation signal can be received by the transponders with adequate strength. In Fig. 2 seven transponders T1 to T7 exist in the interrogation zone 14, these transponders being capable of receiving the RF interrogation signal with sufficient field strength so that they can react thereto accordingly. Three further transponders T8 to T10 are located outside of the interrogation zone 14 so that they cannot be detected in this given spatial arrangement.

[0020] Referring now to Fig. 3 the contents of the RF interrogation signal sent by the interrogator 10 are evident. The signal usually begins with a code string characterizing the start of the signal to be transmitted. This code string is usually made use of as the start string and consists of 5 bits. Following this code string are 8 flag bits, this being followed in turn by the transponder address consisting of 32 or also 64 bits. Following this in transmission are 8 instruction bits which may be followed by up to 12 instruction parameters. In addition 0 to 64 data bits may follow the instruction parameters. Also transmitted is a check code making use of 16 bits generated by the check sum method from all of the bits of the interrogation signal and usually termed cyclic redundancy check (CRC). The last code string transmitted is an end string of 4 bits characterizing the end of the signal.

[0021] When, in making use of the identification method as described in conjunction with Fig. 1, the interrogator 10 has received a transponder response, then this response has already informed the interrogator as to the full address of the transponder. It has also received with the response signal the CRC check code string from the transponder which in generating this code string used the same algorithm as the interrogator. In addition to sending its response signal the responding transponder is in a condition in which it is ready to receive further RF signals. The transponder is thus able to receive an instruction sent by the interrogator which places it in the resting status. However, so that only this transponder reacts to the resting status instruction, the signal sent by the interrogator must of course contain a signal section identifying the address of the responding transponder, since it is only when this is assured that it is certain that only this transponder reacts to the muting instruction.

[0022] As already explained in detail above with reference to Fig. 1, in identifying the transponders partial addresses are generated and a transponder will already respond when the generated partial address agrees with a corresponding section of its full address. When,

for instance, the interrogator has prompted the four least-significant bits of the partial address relating to the full address to be generated in the transponder, and the transponder has detected agreement in this address section, then it responds by sending its full address which is received by the interrogator. The interrogator now knows that this transponder is contained in the interrogation zone 14. Now, in identifying further transponders, it does not immediately prompt the next partial address to be generated, which could possibly agree with partial addresses of other transponders, it instead sending the remaining bits of the address of the detected transponder, i.e. less the bits of the partial address, together with an instruction resulting in the transponder being muted. The transponder that has just responded will thereby "see" that the received remaining address together with the already generated partial address corresponds to its full address so that it reacts accordingly to the instruction and assumes the resting status.

[0023] Sending the remaining address takes up much less time than a complete addressing procedure which would necessitate sending the complete RF interrogation signal as shown in Fig. 3.

[0024] It is not until this transponder has been signalled into the resting status that the interrogator sends a code string serving as a trigger signal TG which prompts the transponders remaining in the interrogation zone to generate the next partial address. This signal is the trigger signal TG sent at the end of each complete RF interrogation signal, made up of 4 bits. However, use may also be made of simply an ID pulse for this purpose which is followed by a pulse pause of a predetermined duration.

[0025] Instead of sending the remaining address together with the muting instruction the check code string CRC can also be sent to the transponder having previously responded by returning its complete address. This transponder has namely sent this check code string CRC directly prior together with its address so that it recognizes it as part of the signal sent by the interrogator belonging to its address and reacts to the muting instruction by assuming the resting status.

[0026] Referring now to Fig. 4 there is illustrated a time plot illustrating schematically a section of an identification procedure, on the one hand, for the case of a transponder response being received and, on the other, for the case of no transponder response being received.

[0027] In an identification procedure it is always the case that the interrogator sends a pulse P at the start of the RF interrogation signal sent specially for this case, this pulse being interpreted in the transponders as a sync pulse. Immediately following this pulse P is the trigger signal TG which prompts the transponders to generate a partial address. As evident from Fig. 4 at A this energy pulse string is followed by a waiting period W in the course of which the interrogator waits to receive the transponder response. As evident from Fig. 4 at B a

response signal A is sent from a transponder in the present example in this waiting period and received by the interrogator. As already mentioned, this response signal contains the full transponder response and a CRC check code string generated from this address.

[0028] In the interrogator a new identification procedure is now initiated, commencing with sending the pulse P. This is followed by the interrogator sending a code string CA identifying the address received previously from the transponder. As already mentioned this code string CA is the remaining address or the CRC check code string likewise received previously. In addition, this code string CA contains the muting instruction which prompts the transponder to assume the resting status. As soon as this code string CA has been sent, the interrogation signal sends the energy pulse string EOF which prompts all transponders still to assume the resting status to generate the next partial address in sequence. This energy pulse string EOF is followed by a further waiting period W, in the course of which no transponder response is received, however, in the example as shown in Fig. 4, because no transponder exists in the interrogation zone 14 having an address section agreeing with the newly generated partial address. On timeout of the waiting period W the interrogator again generates the pulse P and it sends immediately following this pulse the energy pulse string EOF which prompts the next partial address in sequence to be generated in the transponders.

[0029] The factor decisively speeding up identifying in the method as described is that the time period in which the signal CA is sent from the interrogator to the transponder to be signalled into the resting status can be maintained very short since now in this period not a complete RF interrogation signal, as shown in Fig. 3, but merely a code string identifying the address of the responding transponder needs to be sent which is short as compared to this complete signal.

[0030] Once, in the course of the interrogation procedures, all transponders existing in the interrogation zone 14 have been identified and signalled into the resting status, the interrogator again implements a full interrogation cycle which as a rule takes up only very little time since actually no further transponder should be there to respond. Should a further transponder response still be received, however, due to a hit not having been recognized for the reasons as described above in the previous identification cycle, then this transponder too can also be identified, after which the addresses of all transponders existing in the interrogation zone 14 have been reliably identified.

[0031] The method as described above takes up so little time that it may also be put to use successfully when a larger number of transponders exists in the interrogation zone 14. For example, it is sufficient to merely transmit the 16 bit check code string CRC for a 64 bit address. In addition, identifying is also possible when the transponders are moving relating to the inter-

rogator so that a specific number of transponders exists in the interrogation zone 14 only temporarily.

Claims

1. A method of identifying several transponders located in the interrogation zone of an interrogator which for implementing an identification procedure sends an RF interrogation signal containing energy pulses for generating a supply voltage in the transponders and a data message having an address identifying a transponder, whereby a transponder containing this address reacts to receiving the RF interrogation signal by sending a response signal containing the transponder address and the data stored in the transponder, whilst for implementing an identification procedure an RF interrogation signal is sent by the interrogator containing at least one pulse which is used in the transponders to generate partial addresses in sequence, whereby a transponder reacts by sending its response signal in reaction to the partial address generated therein only when the partial address agrees with a corresponding section of its address,
characterized in that said interrogator sends the at least one pulse for incrementing to the next partial address in sequence in said transponder directly on timeout of a time period in which said interrogator is waiting for a transponder response when it has received no transponder response, whilst when having received a transponder response it sends an RF interrogation signal containing a signal section to be recognized by said responding transponder as a code string characterizing its address and a data section, and it not being until thereafter that it sends at least one pulse activating said incrementation to said next partial address in sequence, and that in said data section an instruction is sent to said responding transponder which places it in the condition in which on receiving an RF interrogation signal containing its address it does not react by sending its response signal.
2. The method as set forth in claim 1, characterized in that the remainder of said address after cutting off said partial address is said code string characterizing said address of said responding transponder.
3. The method as set forth in claim 1, characterized in that a check code string contained in said RF response signal is said code string characterizing said address of said responding transponder, this check code string agreeing with a check code string formed in said transponder when sending its response signal.

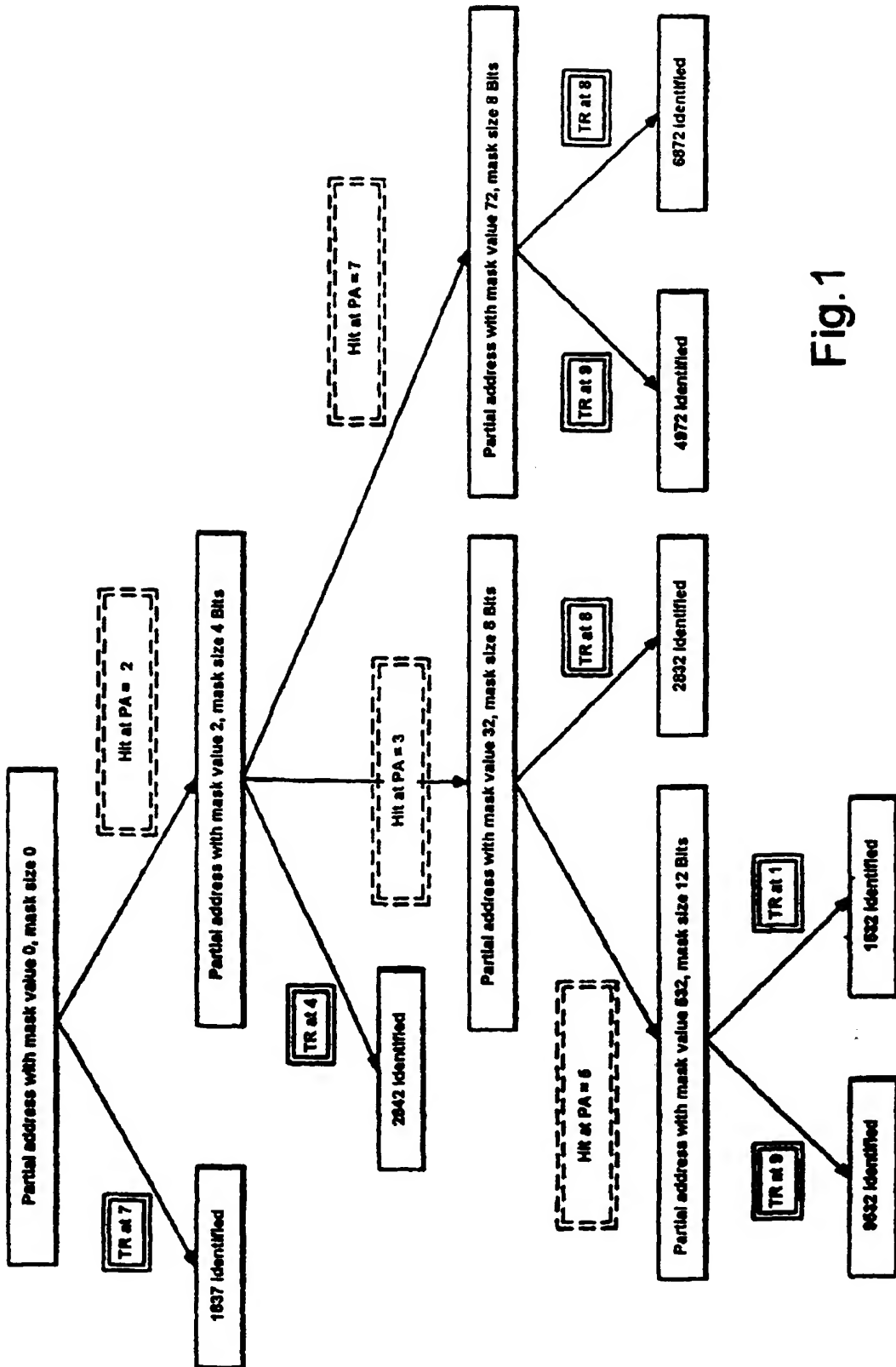


Fig.1

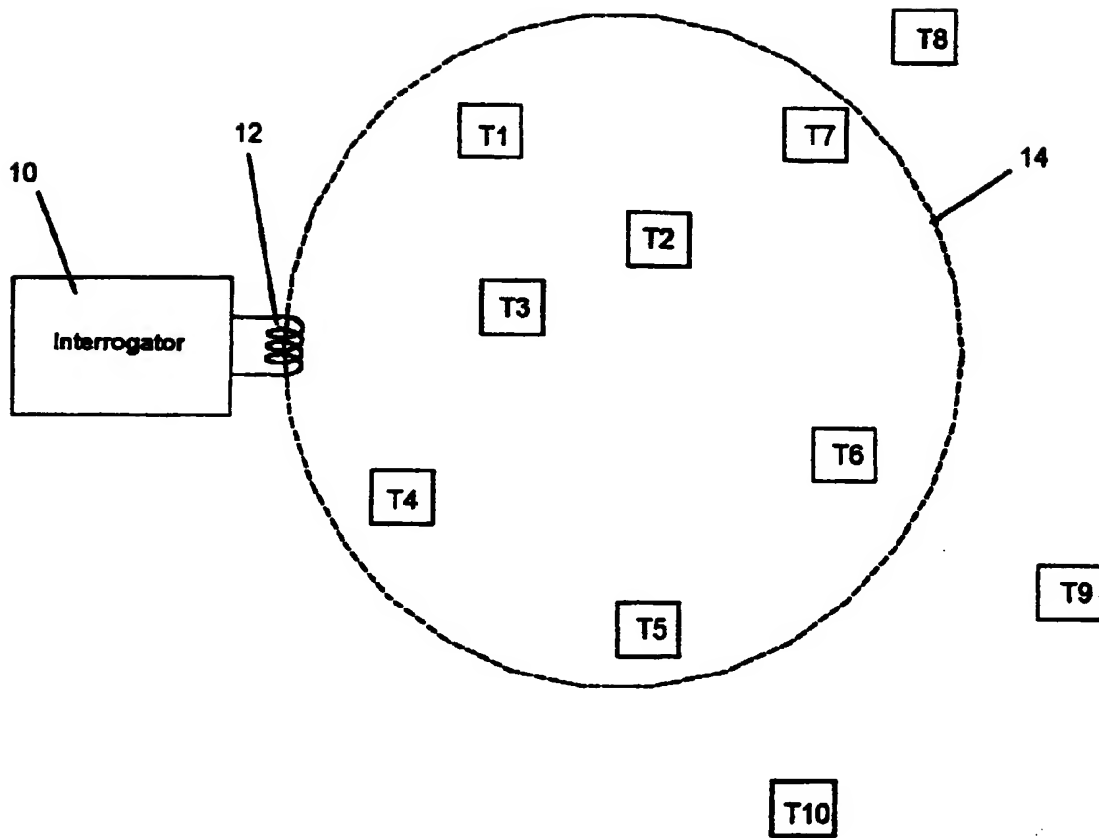


Fig.2



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 00 12 1299

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
D,A	EP 0 831 618 A (TEXAS INSTRUMENTS DEUTSCHLAND) 25 March 1998 (1998-03-25) * abstract; figure 5 *	1-3	G06K7/00
D,A	EP 0 919 943 A (TEXAS INSTRUMENTS DEUTSCHLAND) 2 June 1999 (1999-06-02) * abstract; claims 1-4 *	1-3	
A	US 5 339 073 A (DODD HAROLD ET AL) 16 August 1994 (1994-08-16) * column 5, line 7 - line 61; figures 2,3 *	1-3	
A	US 5 966 083 A (LENARCIK ANDRZEJ ET AL) 12 October 1999 (1999-10-12) * abstract; claim 1 *	1	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			G06K
Place of search		Date of completion of the search	Examiner
THE HAGUE		8 January 2001	Chiarizia, S
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**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 00 12 1299

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

08-01-2001

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0831618 A	25-03-1998	JP 10126308 A	15-05-1998
EP 0919943 A	02-06-1999	NONE	
US 5339073 A	16-08-1994	CA 1337946 A	16-01-1996
		AT 110480 T	15-09-1994
		DE 3851168 D	29-09-1994
		DE 3851168 T	30-03-1995
		EP 0285419 A	05-10-1988
		GB 2202981 A,B	05-10-1988
US 5966083 A	12-10-1999	US 5995017 A	30-11-1999
		AT 145741 T	15-12-1996
		AU 658857 B	04-05-1995
		CA 2058692 A	05-07-1992
		DE 69215388 D	09-01-1997
		DK 494114 T	23-12-1996
		EP 0494114 A	08-07-1992
		EP 0685825 A	06-12-1995
		ES 2099792 T	01-06-1997
		GR 3022331 T	30-04-1997
		JP 3100716 B	23-10-2000
		JP 4315081 A	06-11-1992
		SG 48423 A	17-04-1998
		US 5537105 A	16-07-1996
		ZA 9200039 A	28-10-1992